DA00016816-001



WAVE FACTORY

MULTIFUNCTION GENERATOR

WF1973/WF1974

Specifications

NF Corporation

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1. OVERVIEW

1.1 General

The WF1973 and WF1974 are multifunctional generators based on direct digital synthesizers (DDS).

The WF1973 is a 1-channel generator, while the WF1974 is a two-channel generator.

1.2 Features

- Highest frequency: 30 MHz (sine wave), 15 MHz (square wave, pulse)
- Frequency accuracy: $\pm(3 \text{ ppm} + 2 \text{ pHz})$, high resolution of 0.01 μ Hz. 10 MHz external frequency reference can be used.
- Maximum output voltage: 20 Vp-p/open, 10 Vp-p/50 Ω
- Large number of standard parameter-variable waveforms: Sine wave, square wave (variable duty), pulse (variable pulse width/duty, leading edge time, trailing edge time), ramp wave (variable symmetry), CF controlled sine wave (variable crest factor), staircase sine wave (variable number of steps), Gaussian pulse (variable σ), Sin(x)/x (variable number of zero crossings), exponential rise/fall (variable time constant), damped oscillation (variable oscillation frequency, damping time constant), pulse surge (variable rising and duration times), trapezoid (variable rise, fall, and upper base width), and so on.
- Large-capacity arbitrary waveform memory: 512 K words max., saving capacity: 128 waveforms/4 M words
- Phase and waveform remain continuous even when frequency is changed or during frequency sweep.
- Square wave, pulse with variable duty and high resolution of 0.0001%
- Pulse with variable leading edge time and trailing edge time
- Various oscillation modes
 - Continuous oscillation
 - Modulation: FM, FSK, PM, PSK, AM, DC offset modulation, PWM
 - Sweep: Frequency, phase, amplitude, DC offset, duty
 - Burst oscillation: Auto burst, trigger burst, gate oscillation, triggered gate oscillation

 \bullet Sequence oscillation: Variable waveform/ frequency/ phase/ amplitude/ DC offset/ square wave duty, constant value/ linear interpolation, jump/ repeat/ hold/ branch

• Sequence function for easy test waveform creation and adjustment Flexible waveform creation possible through combination with standard parametervariable waveforms

Frequency, phase, amplitude, etc., can be rapidly changed and swept

- Intuitive user interface through use of high-resolution QVGA TFT color LCD
- Two-channel ganged function with 2 phases, constant frequency difference, constant frequency ratio, and differential output (only WF1974)
- Floated from housing for each channel to reduce effect of ground loop
- Multiple-phase oscillator can be configured by synchronizing multiple units
- USB and GPIB interfaces provided
- Thin and lightweight: Height of approx. 9 cm, weight of approx. 2.1 kg

2. CONFIGURATION

Main unit1
Accessories
Instruction Manual (Basics)1
CD(PDF instruction manuals, application software)1
PDF instruction manuals :
Basics, Application, Remote Control, Arbitrary Waveform Editing
Software, Sequence Editing Software, LabVIEW Driver
Application software :
Arbitrary Waveform Editing Software,
Sequence Editing Software, LabVIEW Driver
Power cord set(2m, with 3-prong plug)1

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3. SPECIFICATIONS

The values of items marked with *1 are guaranteed values. All other values are either nominal values or typical (typ.) values, and are not guaranteed. Conditions unless otherwise mentioned are as follows: Continuous oscillation, 50 Ω load, 10 Vp-p/50 Ω amplitude setting, 0 V DC offset setting, auto-range, ±FS waveform amplitude range, external addition off, AC voltage = RMS value measurement.

Oscillation Modes 3.1

Continuous, modulated, sweep, burst, sequence

3.2 Waveforms

3.2.1	Standard waveforms	
r	Types	Sine, square, pulse, ramp, parameter-variable waveform, noise (Gaussian distribution), DC
]	Polarity	Normal, inverted (selectable) (excluding DC)
1	Amplitude range	$-FS/0, \pm FS, 0/+FS$ (selectable) (excluding DC)
3.2.2	Arbitrary waveforms	
,	Waveform length	4 K to 512 K words $(2^{n}, n = 12 \text{ to } 19)$ or
		2 to 10,000 control points (linear interpolation
		between control points)
r	Total waveform saving capacity	Up to 128 waveforms or 4 M words (combined total for
		channels 1 and 2)
		Saved to non-volatile memory
,	Waveform data amplitude resolutio	n 16 bits
ŝ	Sampling rate	120MS/s
]	Polarity	Normal, inverted (selectable)
1	Amplitude range	$-FS/0, \pm FS, 0/+FS$ (selectable)
(Output bandwidth	25 MHz, –3 dB

Frequency, Phase 3.3

Frequency	setting	range
riequency	setting	range

Frequency setting rar	nge			
Oscillation Mode	Continuous,	Sweep (Gated	Sequence	
	Modulated, Sweep	Single-Shot), Burst		
	(Continuous,			
Waveform	Single-Shot)			
Sine	0.01µHz to 30MHz	0.01µHz to 10MHz	0.01µHz to 10MHz	
Square	0.01µHz to 15MHz	0.01µHz to 10MHz	$0.01 \mu Hz$ to $10 MHz$	
Pulse	0.01µHz to 15MHz	0.01µHz to 10MHz	Not usable	
Ramp	0.01µHz	to 5MHz	$0.01 \mu { m Hz}$ to $5 { m MHz}$ *2	
Parameter- variable	0.01µHz	to 5MHz	$0.01 \mu { m Hz}$ to $5 { m MHz}$ *2	
Noise	Fixed t	o 26 MHz equivalent b	oandwidth	
DC		Frequency setting inva		
Arbitrary		0.01µHz to 5MHz		
	*2 : U	Jsed through conversion	on to arbitrary waveforn	
Frequency setting res		0	C C	
Frequency setting by	•		number of the set period	
Frequency accuracy a	-	±(3 ppm of setting + 2 pHz) ±1 ppm/year -1800.000° to +1800.000°(0.001° resolution)		
Frequency aging rate				
Phase setting range				
I make setting range	10001			
3.4 Output Characte	eristics			
3.4.1 Amplitude				
Setting range	0 Vp-p	0 Vp-p to 20 Vp-p/open, 0 Vp-p to 10 Vp-p/50 Ω		
0 0		Peak value combining waveform amplitude and DC offset is limited to ±10 V/open or lower		
Setting resolution		nVp-p or lower 4 dig		
Southing resolution			its or 1 mVp-p	
Accuracy ^{*1}		1 Vp-p or higher 5 digits or 1 mVp-p ±(1% of amplitude setting [Vp-p] + 2 mVp-p)/open		
Accuracy		Condition: 1 kHz sine, amplitude setting of		
	Condit		_	
о: ··	τ7 τ	20 mVp-p/open or	nigner	
Setting units		Vpk, Vrms, dBV, dBm		
Range		Auto, hold (selectable)		
		um output voltage rar		
	Amplit	ude attenuator range	: 0 dB, -10 dB, -20 dE	

Waveform amplitude resolution

Approx. 14 bits Condition:Amplitude setting of 36 mVp-p/open or higher

-30 dB

3.4.2 DC offset	
Setting range	± 10 V/open, ± 5 V/50 Ω
Setting resolution	$\pm 499.9 \text{ mV}$ or lower 4 digits or 0.1 mV
	± 0.5 V or higher 5 digits or 1 mV
Accuracy ^{*1}	\pm (1% of DC offset setting [V] + 5 mV
	+ 0.5% of amplitude setting [Vp-p])/open
	Condition:Sine wave output of 10 MHz or lower, 20°C to 30°C
	Outside 20°C to 30°C temperature range,
	1 mV/°C typ. is added
3.4.3 Load impedance setting	
Functions	Setting and display of the amplitude and DC offset
	for the output termination voltage under the specified
	load condition
Setting range	$1~\Omega$ to $10~k\Omega$ (1 Ω resolution), $50~\Omega,$ High-Z (load open)
3.4.4 Waveform output	
Output on/off control	On, Off (selectable) (When Off, output pin open state)
Output impedance	50 Ω, unbalanced
Short-circuit protection	Protection against short circuit to signal GND
Output connector	Front panel, BNC receptacle
3.4.5 Sync/sub output	
Output signal	Reference phase sync, internal modulation sync,
	burst sync, sweep sync, sequence step sync, internal
	modulation signal, sweep X drive (selectable)
Reference phase sync output wavef	
	Square waveform with 50% duty that rises at zero
	phase position of reference phase (DDS oscillation phase) of waveform output
Output voltage	Sync signals: TTL level (low level of 0.4 V/open or
Output voltage	lower, high level of 2.7 V/open or higher)
	Internal modulation signal: -3 V to +3 V/open
	Sweep X drive: 0 V to $+3$ V/open
Output impedance	50Ω , unbalanced
Load impedance	$50 \ \Omega$ or higher recommended
Output connector	Front panel, BNC receptacle
3.5 Signal Characteristics	
3.5 Signal Characteristics	

3.5.1 Sine wave

Amplitude frequency characteristics \ast_1

1 1 5	
100 kHz or lower	±0.1 dB
$100 \mathrm{~kHz}$ to $5 \mathrm{~MHz}$	$\pm 0.15~\mathrm{dB}$
$5~\mathrm{MHz}$ to $20~\mathrm{MHz}$	$\pm 0.3 \text{ dB}$
20 MHz to 30 MHz	$\pm 0.5~\mathrm{dB}(\pm 0.8~\mathrm{dB}$ at amplitude setting of $2.8~\mathrm{Vp}$ -p/50 Ω
	or higher)
	Condition:Amplitude setting 50 mVp-p to 10 Vp-p/50
	Ω, reference frequency 1 kHz

Total harmonic distortion \ast_1 10 Hz to 20 kHz

0.2% or less Condition:Amplitude setting of 0.5 Vp-p to 10 Vp-p/50 Ω

Harmonic spurious ^{*1} Condition: Amplitude setting 1 MHz or lower 1 MHz to 10 MHz 10 MHz to 30 MHz Non-harmonic spurious ^{*1} 1 MHz or lower 1 MHz to 10 MHz 10 MHz to 30 MHz

0.5 Vp-p to 2 Vp-p/50 Ω -60 dBc or lower -50 dBc or lower -40 dBc or lower 2 Vp-p to 10 Vp-p/50 Ω -60 dBc or lower -43 dBc or lower -30dBc or lower

-60 dBc or lower
-50 dBc or lower
-45 dBc or lower
Condition:Amplitude setting of
0.5 Vp-p to 10 Vp-p/50 Ω

3.5.2 Square wave

Duty	
Variable range selectable	Normal, extended (selectable)
Normal range	Duty can be changed in range with little jitter and no pulse loss. The higher the frequency, the narrower the duty setting range.
Extended range	With 2.5 ns rms or less typ. jitter, duty can be changed always in maximum range. In the case of a pulse width of 8.4 ns or less, loss may occur; on average, it is equal to the set duty.
Setting range	
Normal range	0.0100% to 99.9900% (0.0001% resolution)
	Upper limit (%): 100 – frequency (Hz) / 300,000
	Lower limit (%): frequency (Hz) / 300,000
Extended range	0.0000% to 100.0000% (0.0001% resolution)
Duty accuracy ^{*1}	
100 kHz or lower	$\pm 0.1\%$ of period (duty setting 1% to 99%)
100 kHz to 1 MHz	$\pm 1\%$ of period (duty setting 5% to 95%)
1 MHz to 3 MHz	$\pm 3\%$ of period (duty setting 40% to 60%)
Rising/falling time ^{*1}	17 ns or less
	However, approx. 20 ns in the case of burst oscillation with stop level setting, gated single-shot sweep with stop level setting, and sequence oscillation
Overshoot	5% or less typ.
Jitter	Normal variable duty range : 300 ps rms or less typ. (100 Hz or higher)
	Extended variable duty range : 2.5 ns rms or less typ.

3.5.3Pulse wave

0.0.0	naro	
Pulse widt	h	
Duty s	setting range	0.0170% to 99.9830% (0.0001% resolution)
Time s	setting range	25.50 ns to 99.9830 Ms (0.001% or less of period, or
		0.01 ns resolution)
Leading ed	lge time, trailing edge tin	ne
Settin	g range	15.0 ns to 58.8 Ms (3 digits or 0.1 ns resolution)
		Leading edge time and trailing edge time
		independently settable
Minim	num setting value	Largest of either 0.01% of period or 15 ns
Pulse widt	h, leading edge time, trai	ling edge time limits
The p	edge time, trailing edge time, and period are mutually	
constrained by the following equations.		
The du	se width time / period.	
	(leading edge time + tra	iling edge time) × 0.85 ≤ pulse width time
	pulse width time \leq period	d – (leading edge time + trailing edge time) $\times 0.85$
Overshoot		5% or less typ.
Jitter		500 ps rms or less typ. (10 kHz or higher)
		2.5 ns rms or less typ. (under 10 kHz)
3.5.4 Ramp	wave	

Symmetry setting range 0.00% to 100.00% (0.01% resolution)

3.5.5Parameter-variable waveforms

a) Steady sine group

eady sine group				
Waveform Name	Waveform Example	Description and Variable Parameters		
Unbalanced sine		Waveform for which the amplitudes of the first half cycle and second half cycle of a sine wave can be changed independently		
		First-half amplitude (–100.00% to 100.00%) Second-half amplitude (–100.00% to 100.00%)		
Clipped sine		Waveform obtained by clipping the top and bottom of the amplitude of a sine wave Clip rate (0.00% to 99.99%)		
CF controlled sine		Waveform obtained by extracting only the 90° and 270° neighborhood of a sine wave and expanding the amplitude Crest factor (1.41 to 10.00)		
Conduction angle		Waveform obtained by extracting only the front or back of each half cycle of a sine wave		
controlled sine		Conduction angle (-180.00° to 180.00°) Remark: In the case of a positive/negative conduction angle, back/front conduction angle		
Staircase	-1 5 7	Staircase shaped sine wave		
sine		Number of steps (2 to 100)		
Multi-cycle		Waveform obtained by continuing sine for several cycles		
sine		Number of cycles (0.01 to 50.00) Start phase (-360.00° to 360.00°)		

b) Transient sine group

Waveform Name	Waveform Example	Description and Variable Parameters		
On-phase controlled	- ++	Sine wave with slope into on state		
sine		Complete-on phase (0.00° to 360.00°) On-slope time (0.00% to 50.00% of basic period)		
Off-phase		Sine wave with slope into off state		
controlled sine		Off-phase (0.00° to 360.00°) Off-slope time (0.00% to 50.00% of basic period)		
Chattering-	- 14	Sine wave with chattering into on state		
on sine		On-phase (0.00° to 360.00°) Number of chatterings (0 to 3) On-state time (0.00% to 20.00% of basic period) Off-state time (0.00% to 20.00% of basic period)		
Chattering-		Sine wave with chattering into off state		
off sine		Off-phase (0.00° to 360.00°) Number of chattering (0 to 3) On-state time (0.00% to 20.00% of basic period) Off-state time (0.00% to 20.00% of basic period)		

c) Pulse group

Warrafarm	Warafama	
Waveform	Waveform	Description and Variable Parameters
Name	Example	
Gaussian	- · •	Gaussian distribution waveform
pulse		Standard deviation (0.01% to 100.00% of basic period)
	-+	
T .		
Lorentz	- ++	Lorentz waveform
pulse		Half value of width (0.01% to 100.00% of basic period)
Haversine		Sin ² pulse
maversine		Width (0.01% to 100.00% of basic period)
Half-sine	-1 3 T	Half-sine cycle pulse
pulse	- +	Width (0.01% to 100.00% of basic period)
	-+	
Trapezoid	- ++	Trapezoid pulse
pulse		Slope width (0.00% to 50.00% of basic period)
		Upper base width (0.00% to 100.00% of basic period)
Sin(x)/x		Sin(x)/x waveform
	- ++	Number of zero crossings (1 to 50)

d) Transient response group

ransient response group			
Waveform Name	Waveform Example	Description and Variable Parameters	
Exponential	- 10-7	First order LPF step response waveform	
rise		Time constant (0.01% to 100.00% of basic period)	
Exponential	- 1#T	First order HPF step response waveform	
fall		Time constant (0.01% to 100.00% of basic period)	
Second	- 1#T	Second order LPF step response waveform	
order LPF step response		LPF natural frequency (1.00 to 50.00 times basic frequency) LPF Q (0.50 to 50.00)	
Damped oscillation	- 18	Oscillation waveform with an amplitude that decreases exponentially	
oscination		Oscillation frequency (0.01 to 50.00 times basic frequency) Damping time constant (-100.00% to 100.00% of basic period) Remark: In the case of a negative damping time constant, oscillation waveform with an amplitude that increases exponentially	

e) Surge group

Waveform Name	Waveform Example	Description and Variable Parameters
Oscillation		Surge waveform with damped oscillation
surge		Oscillation frequency (0.01 to 50.00 times basic frequency) Damping time constant (0.01% to 100.00% of basic period) Trailing time constant (0.01% to 100.00% of basic period)
Pulse surge		Pulsed surge waveform
		Rising time (0.01% to 100.00% of basic period) Duration time (0.01% to 100.00% of basic period) Remark: The rising time represents the time from the 10% threshold to the 90% threshold of the rising edge. The duration time represents the time from 10% threshold of the rising edge to the 10% threshold of the next falling edge.

f) Others group

there group			
Waveform Name	Waveform Example	Description and Variable Parameters	
Trapezoid		Trapezoid waveform with offset in the amplitude direction	
with offset		Leading delay (0.00% to 100.00% of basic period) Rising-slope width (0.00% to 100.00% of basic period) Upper base width (0.00% to 100.00% of basic period) Falling-slope width (0.00% to 100.00% of basic period) Offset (0.00% to 100.00%)	
Half-sine		Pulse whose rise and fall are half-sine waveform	
edge pulse		Leading edge time (0.00% to 100.00% of basic period) Trailing edge time (0.00% to 100.00% of basic period) Duty (0.00% to 100.00%)	
Bottom		Ramp waveform with bottom level as reference	
referenced ramp		Symmetry(0.00% to 100.00%)	

3.6	Modulated Oscillation Mode	
3.6.	1 General	
	Modulation type	FM, FSK, PM, PSK, AM, DC offset modulation, PWM
	Modulation source	Internal, external (selectable)
	Internal modulation waveform	
	Other than FSK, PSK	Sine wave, square wave (50% duty), triangular wave (50% symmetry), rising ramp wave, falling ramp wave, noise, arbitrary wave
	FSK, PSK	Square wave (50% duty)
	Internal modulation frequency	
	Other than FSK, PSK	0.1 mHz to 100 kHz (5 digits or 0.1 mHz resolution)
	FSK, PSK	0.1 mHz to 1 MHz (5 digits or 0.1 mHz resolution)
	Internal modulation sync output	
	Output waveform	Square wave with 50% duty that rises at zero phase position of internal modulation waveform
		Fixed to low level while internal modulation
		waveform is noise
	Output connector Internal modulation signal output	Shared with sync/sub-output connector
	Output voltage	–3 V to +3 V/open
	Output voltage Output connector	Shared with sync/sub-output connector
	External modulation input (other th	
	Input voltage range	±1 V full scale
	Maximum allowed input	$\pm 2 \text{ V}$
	Input impedance	$10 \text{ k}\Omega$, unbalanced
	Input frequency	DC to 25 kHz
	Input connector	Front panel (WF1973) / rear panel (WF1974)
	-	BNC receptacle
		Shared with external addition input, cannot be used
		simultaneously with adding operation
	External modulation input (FSK, PS	SK)
	Polarity	Positive, negative (selectable)
	Input frequency	DC to 1 MHz
	Input connector	Use of external trigger input.
		Input voltage and input impedance follow the
		external trigger input specifications.

3.6.2 Modulation conditions

FM

Carrier waveform	Standard waveform other than noise, pulse wave and
	DC, and arbitrary waveform
Peak deviation setting range	0.00 μ Hz to less than 15 MHz (8 digits or 0.01 μ Hz
	resolution)

	FSK	
	Carrier waveform	Standard waveform other than noise, pulse wave and DC, and arbitrary waveform
	Hop frequency setting range	Within settable carrier waveform frequency range (8 digits or 0.01 µHz resolution)
	PM	
	Carrier waveform	Standard waveform other than noise and DC, and arbitrary waveform
	Peak deviation setting range PSK	0.000° to 180.000° (0.001° resolution)
	Carrier waveform	Standard waveform other than noise and DC, and arbitrary waveform
	Deviation setting range Remark	-1800.00° to +1800.000°(0.001° resolution) The sine wave amplitude frequency characteristics during PSK are limited to 25 MHz, -3 dB.
	AM(non-DSB-SC)	
	Carrier waveform	Standard waveform other than DC, and arbitrary waveform
	Modulation depth setting range Remark	0.0% to $100.0%$ ($0.1%$ resolution) When the modulation depth is 0% , the amplitude is $1/2$ of the set value.
	AM(DSB-SC) (Double Side Band - Su	
	Carrier waveform	Standard waveform other than DC, and arbitrary waveform
	Modulation depth setting range Remark	0.0% to 100.0% (0.1% resolution) When the modulation depth is 100%, the maximum amplitude is equal to the set value. During DSB-SC, the carrier frequency component is zero.
	DC offset modulation	
	Carrier waveform	Standard waveform and arbitrary waveform
	Peak deviation setting range PWM	0 V to 10 V/open
	Carrier waveform Peak deviation setting range Square wave	Square wave, pulse wave
	Normal variable duty range	0.0000% to 49.9900% (0.0001% resolution)
	Extended variable duty range Pulse wave	0.0000% to 50.0000% (0.0001% resolution) 0.0000% to 49.9000% (0.0001% resolution)
3.7	Sweep Oscillation Mode	
3.7.	l General	
	Sweep types	Frequency, phase, amplitude, DC offset, duty
	Sweep functions	One-way (ramp waveform shape), shuttle(triangular waveform shape) (selectable) Linear, log (frequency sweep only) (selectable)
	Sweep range setting	Start value and stop value specification Center value and span value specification
	Sweep time setting range	0.1 ms to 10,000s (4 digits or 0.1 ms resolution)

Sweep mode	Continuous, single-shot, gated single-shot
1	(selectable)
	During gated single-shot, oscillation occurs only
	during sweep execution
Operation	Start, stop, hold/resume, start value output, stop
-	value output
Trigger source (used for single-shot sweep and gated single-shot sweep)	
	Internal, external (selectable)
	Trigger delay setting is invalid. Manual trigger
	possible.
Internal trigger oscillator for sweep	r · · · · · · ·
(used for single-shot sweep and gate	ed single-shot sweep)
Period setting range	100.0 µs to 10,000 s (5 digits or 0.1 µs resolution)
Stop level setting (used for gated sin	
Function	Specification of signal level while oscillation is
	stopped during gated single-shot sweep
Setting range	-100.00% to $+100.00%$ of amplitude full scale (0.01%)
Setting range	resolution) or off
Oscillation stop unit during gated si	
osemation stop unit during gated s	1 cycle, 0.5 cycles (selectable)
Sweep sync/marker output	i cycle, 0.5 cycles (selectable)
Marker off, one-way sweep	Low level from sweep start value to half of sweep
Marker on, one-way sweep	time.
	High level at any other time.
Marker off, shuttle sweep	Low level from sweep start value to sweep stop value.
Marker on, shuttle sweep	High level at any other time.
Marker on	
Marker on	Low level from sweep start value until marker value.
Output compactor	High level at any other time.
Output connector	Shared with sync/sub-output connector
Sweep X drive output	0 W to 12 W/more
Output voltage	0 V to +3 V/open
	$0 \text{ V} \rightarrow +3 \text{ V}$ during sweep value rise
	$+3 \text{ V} \rightarrow 0 \text{ V}$ during sweep value fall
Output connector	Shared with sync/sub-output connector
Sweep external control input	
Input connector	Use of 3 bits of multi-I/O connector
Control items	Start, stop, hold/resume
	for single-shot sweep and gated single-shot sweep)
Polarity	Positive, negative, off (selectable)
Input connector	Use of external trigger input.
	Input voltage and input impedance follow the
	external trigger input specifications.
.7.2 Sweep conditions	
Frequency sweep	
Waveform	Standard waveform other than noise, pulse wave, and
	DC, and arbitrary waveform
Start, stop frequency setting range	$0.01 \ \mu Hz$ to $30 \ MHz$ (0.01 μHz resolution)
Phase sweep	
Waveform	Standard waveform other than noise and DC, and
	arbitrary waveform
Start, stop phase setting range	–1800.000° to 1800.000° (0.001° resolution)
	. , , , , , , , , , , , , , , , , , , ,

Amplitude sweep Waveform	Standard waveform other than DC, and arbitra
	waveform
Start, stop amplitude setting range	0 Vp-p to 20 Vp-p/open
DC offset sweep	
Waveform	Standard waveform and arbitrary waveform
Start, stop DC offset setting range	-10 V to +10 V/open
Duty sweep	
Waveform	Square wave, pulse wave
Start, stop duty setting range	1 / 1
Square wave	
Normal variable duty range	0.0100% to 99.9900% (0.0001% resolution)
	0.0000% to 100.0000% (0.0001% resolution)
Pulse wave	0.0170% to 99.9830% (0.0001% resolution)
Burst Oscillation Mode	
Burst mode	
Auto burst	Repeats oscillation of mark wave number an
	oscillation stop of space wave number.
	Trigger invalid.
Trigger burst	Performs oscillation of mark wave number in sy
	with trigger.
Gate	Performs oscillations in cycles of integers or integ
	multiples of half-cycles, in sync with the gate signal
	However, if the waveform is noise, oscillation on/o
	operation is done through the gate signal.
Triggered gate	Gate oscillation switched on/off by gate upon trigger
Target waveforms	
Auto, trigger burst	Standard waveform other than noise and DC, an
	arbitrary waveform
Gate, triggered gate	Standard waveform other than DC, and arbitra
	waveform
	0.5 cycles to 999,999.5 cycles, in 0.5-cycle units
	0.5 cycles to 999,999.5 cycles, in 0.5-cycle units
Oscillation stop unit during gate Oscillation start/stop phase setting r	1 cycle, 0.5 cycles (selectable)
Oscillation start/stop phase setting r	-1800.000° to +1800.000° (0.001° resolution)
	Remark:Same setting value as phase setting
	section 3.3
Stop level setting range	Section 5.5
Function	Specification of signal level when oscillation
	stopped
Setting range	-100.00% to $+100.00%$ of amplitude full-scale (0.01
0 0	resolution) or off
	When the stop level is set to off, stop occurs at the s
	oscillation start/stop phase
Trigger source (used during other the	
	Internal, external (selectable). Manual trigg
	possible.
	-
Internal trigger oscillator for burst (used during other than auto burst)

	Trigger delay setting range	 0.00 μs to 100.00 s (5 digits or 0.01 μs resolution) Latent delay of 0.55 μs Only valid for trigger burst (not valid for gate, triggered gate) Valid for both internal and external trigger sources Not valid for manual trigger
	Trigger jitter	1 ns rms or less typ.
	Burst sync output	
	Polarity	Low level during oscillation. High level at all other times.
	Output connector	Shared with sync/sub-output connector
3.9	Triggers	
	External trigger input	
	Applications	Used for single-shot sweep, gated single-shot sweep, trigger burst, gate, triggered gate, and sequence
	Input voltage	TTL level (low level of 0.8 V or lower, high level of 2.6 V or higher)
	Maximum allowed input	-0.5 V to +5.5 V
	Polarity	Positive, negative, off (selectable) FSK and PSK, sweep, sequence (independently
	Minimum nulse width	settable) 50 ns
	Minimum pulse width	
	Input impedance	10 kΩ (pulled up to +3.3 V), unbalanced Front panel (WF1973) / rear panel (WF1974)
	Input connector	BNC receptacle
	Manual trigger	Panel key operation
	Applications	Used for single-shot sweep, gated single-shot sweep, trigger burst, gate, triggered gate
	Internal trigger oscillator	Independent for sweep and burst Refer to internal trigger oscillator of each section
3.10) Sequence	
	Number of saved sequences	10 sequences (saved to non-volatile memory)
	Maximum number of steps	Maximum of 255 steps per sequence (not including step of pre-start status)
	Inter-channel operation	In sequence mode, the mode of both channels is the sequence mode. Step control is done in common for both channels.
	Step control parameters	Step time, hold operation, jump destination, number of jumps, step stop phase, branch operation, step termination control, step sync code output
	Intra-step channel parameters	Waveform, frequency, phase, amplitude, DC offset, square wave duty
	In-step operations	Constant, keep, linear interpolation (except

xcep waveform switching) rp (6

Step time setting range	0.1 ms to 1,000 s (4 digits or 0.01 ms resolution)
Jump count setting range	1 to 999 or infinite
Step stop phase setting range	0.000° to 360.000° (CH1 reference phase. 0.001° resolution) or invalid
Branch operation	
State branch	Check of state branch input from multi-I/O connector at step end. Upon branch input detection, branching to specified destination step.
Event branch	Immediate branching to specified destination step through event branch manipulation or input
Control of step termination	Sequence end or transition to next step
Step sync code output	Output of 4-bit code specified for each step to multi-
	I/O connector
	LSB outputtable to sync/sub-output connector
Usable waveforms	Sine wave, square wave, noise, DC, and arbitrary
	wave
	Ramp wave and parameter-variable waveform can be
	used through saving as arbitrary waveforms
Maximum number of usable wavefo	rms
	128
Step start phase	Oscillation start from reference phase 0° of each
	channel at next step after DC or noise (excluding DC
	and noise)
Sequence manipulations	Start, stop, hold/resume, event branch
Sequence external control	
Input connector	Use of 4 bits of multi-I/O connector
Control items	Start or state branch, stop, hold/resume, event
	branch
Sequence external trigger input (sta	art trigger)
Polarity	Positive, negative, off (selectable)
Input connector	Use of external trigger input on CH1 side. Input
	voltage and input impedance follow the external
	trigger input specifications.

3.11 Other I/Os

External 10 MHz frequency reference input

Frequency reference selection	External reference enable, disable (selectable)
Input voltage	0.5 Vp-p to 5 Vp-p
Maximum allowed input	10 Vp-p
Input impedance	1 k Ω , unbalanced, AC coupled
Input frequency	10 MHz (±0.5% (±50 kHz))
Input waveform	Sine wave or square wave (50 \pm 5% duty)
Input connector	Rear panel, BNC receptacle
-	

Frequency reference output (for synchronizing multiple WF1973, WF1974 units)		
Output voltage	1 Vp-p/50 Ω square wave	
Output impedance	50 Ω , AC coupled	
Output frequency	10 MHz	
Output connector	Real panel, BNC receptacle	
External addition input		
Addition gain	$\times 2$, $\times 10$, off (selectable)	
	During ×2, the maximum output voltage range is	
	fixed to 4 Vp-p, and during ×10, 20 Vp-p.	
	Off during sequence oscillation	
Input voltage	-1 V to +1 V	
Maximum allowed input	$\pm 2 \text{ V}$	
Input frequency	DC to 10 MHz (-3 dB)	
Input impedance	10 k Ω , unbalanced	
Input connector	Front panel (WF1973) / rear panel (WF1974)	
	BNC receptacle	
	Shared with external modulation input, cannot be	
	used during external modulation	
Multi-I/O		
Applications	Sweep control, sequence control	
Input voltage	TTL level (low level of 0.8 V or lower, high level of	
	2.6 V or higher. Pulled up to +5 V through 10 k Ω)	
Maximum allowed input	-0.5V to $+5.5$ V	
Output voltage	TTL level (low level of 0.4 V/open or lower, high level	
	of 2.7 V/open or higher)	
Connector	Rear panel, Mini-Dsub 15-pin multiconnector	

3.12 2-channel ganged operation (WF1974 only)

Channel modes

Channel modes	Operation
Independent	Independent setting
2-phase	Holds same frequency. During frequency sweep, internal frequency modulation, and internal FSK, controls to hold the
	same frequency.
	External frequency modulation and external FSK are not possible.
	Phase independently set for each channel.
Constant	Holds the frequency difference as a constant value. During
frequency	frequency sweep, internal frequency modulation, and internal
difference	FSK, controls to hold the frequency difference.
	External frequency modulation and external FSK are not possible.
Constant	Holds the frequency ratio as a constant value. During
frequency ratio	frequency sweep, internal frequency modulation, and internal
	FSK, controls to hold the frequency ratio.
	External frequency modulation and external FSK are not
	possible.
Differential	Same frequency, amplitude, and DC offset. Reverse phase
Output	waveform.
	Controls to hold differential output during all types of sweep
	and internal modulation.
	External modulation and external addition are not possible.

Common limiting conditions during 2-phase, constant frequency difference, constant frequency ratio, and differential output

- Oscillation in same oscillation mode (also same modulation type during modulated oscillation, and same sweep type during sweep oscillation)
- Applicable to standard waveform other than noise and DC, and arbitrary waveform
- Burst, gated single-shot sweep not possible

Same value setting, same manipulation

	Yes			
Frequency difference setting range	$0.00 \ \mu\text{Hz}$ to less than 30 MHz (0.01 μHz resolution)			
	CH2 frequency – CH1 frequency			
Frequency ratio N:M setting range	1 to 9,999,999 (for each of N and M)			
	N:M = CH2 frequency:CH1 frequency			
Phase synchronization	Automatically executed during channel mode			
	switching			
Time difference between channels during 2-phase *1				
	± 20 ng on logg (± 10 ng on logg true)			

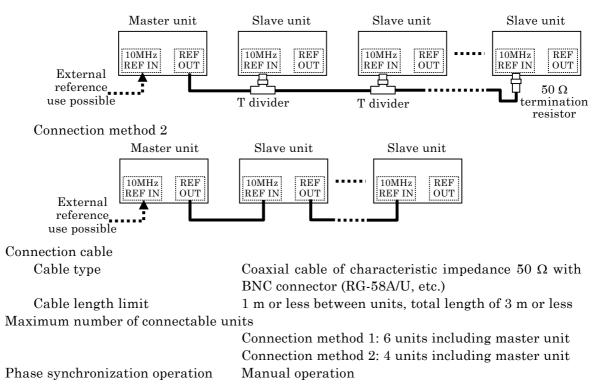
 ± 20 ns or less (± 10 ns or less typ.)

Condition:Same waveform (sine or square)

3.13 Synchronous Operation of Multiple Units

Connection

Connection method 1



Time difference between units

Delay of each channel of nth slave unit in relation to each channel of the master unit $(1 \le N)$

Connection method 1: $31 \text{ ns} + (N - 1) \times 6 \text{ ns} \pm 25 \text{ ns or less typ.}$				
Connection method 2:				
$31 \text{ ns} + (N-1) \times 31 \text{ ns} \pm 25 \text{ ns or less typ.}$				
Condition:Same frequency, same phase, same				
waveform (sine or square), length of				
connection cable between frequency				
reference output and external frequency				
reference input = 1 m (RG-58A/U)				
, ,				

User - Defined Units 3.14

Function	Setting and display in arbitrary unit according to the
	specified conversion expression
Setting target	Frequency (Hz), period (sec), amplitude (Vp-p, Vpk),
	DC offset (V), phase (deg), duty (%)
Conversion expression	[(Setting target value) + n] \times m, or
	$[\log_{10} (\text{setting target value}) + n] \times m$
	Specification of conversion expression and values of n
	and m
Unit character string	Up to 4 characters can be set

Other Functions 3.15

Setting saving capacity External control

3.16 Options

PA-001-1318 multi-I/O cable

10 settings(saved to non-volatile memory) GPIB, USBTMC (SCPI-1999, IEEE-488.2)

Cable with connector on one end, for connection to multi-I/O connector on rear panel. 2 m length. Cut off at one end

3.17 General Characteristics

Display unit 3.5 inch TFT color LCD

I/O ground

The signal grounds for waveform output (FCTN OUT), sync/sub-output (SYNC/SUB OUT), and external modulation/addition input (MOD/ADD IN) are insulated from the housing. These signal grounds are shared within the same channel.

The signal ground for the external 10 MHz reference input (10 MHz REF IN) is insulated from the housing.

Each of the signal grounds of CH1, CH2, and 10MHz REF IN are independent.

The withstand voltage between insulated signal grounds and between housings is 42 Vpk max. (DC + AC peak)

The other signal grounds are connected to the housing.

Power supply

11 0	
Power supply voltage range	100 V AC to 230 V AC $\pm 10\%$ (250 V or lower)
Power supply frequency range	$50 \text{ Hz}/60 \text{ Hz} \pm 2 \text{ Hz}$
Power consumption	WF1973: 50 VA or less
	WF1974: 75 VA or less
Overvoltage category	II

Ambient temperature and humidity ranges

Operation guarantee

Storage conditions

0°C to +40°C, 5%RH to 85%RH Absolute humidity of 1 g/m³ to 25 g/m³,

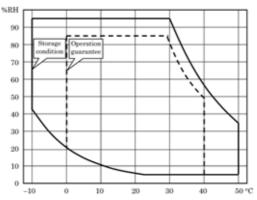
no condensation

Temperature range limitations apply for some specifications.

 $-10^{\circ}\mathrm{C}$ to +50°C, 5%RH to 95%RH

Absolute humidity of 1 g/m³ to 29 g/m³,

no condensation



Warm-up time Pollution degree External dimensions Weight

30 minutes or more typ.

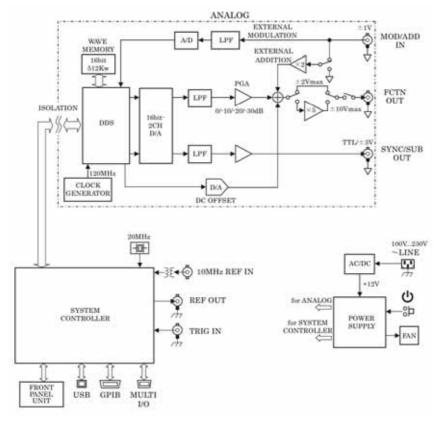
2

216 (W) \times 88 (H) \times 332 (D) mm (excluding projections) Approx. 2.1 kg (excluding accessories, weight of main unit only)

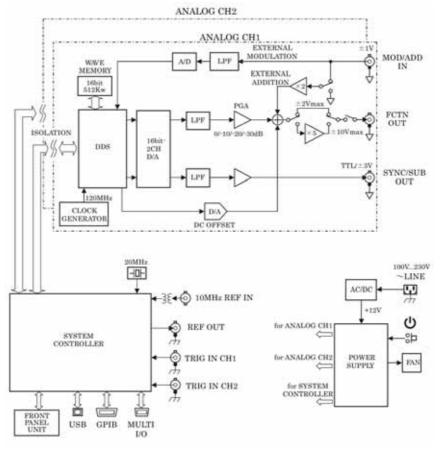
Safety and EMC

Applied only for models with CE marking on their rear panelsSafetyEN61010-1:2001EMCEN61326:1997 + A1:1998 + A2:2001 + A3:2003

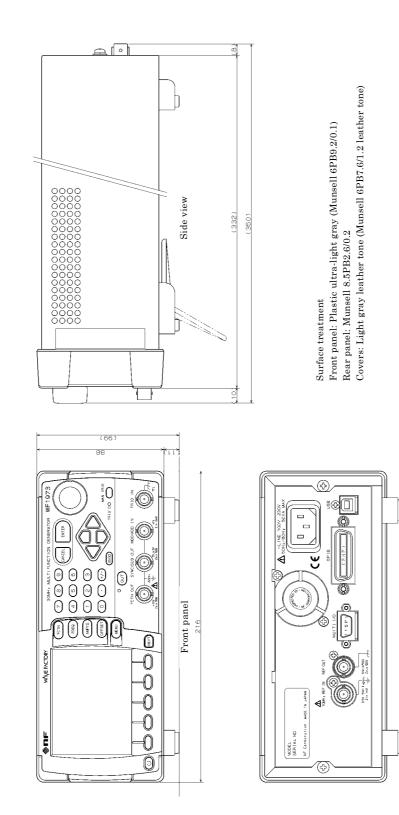
Block diagram (WF1973)



Block diagram (WF1974)

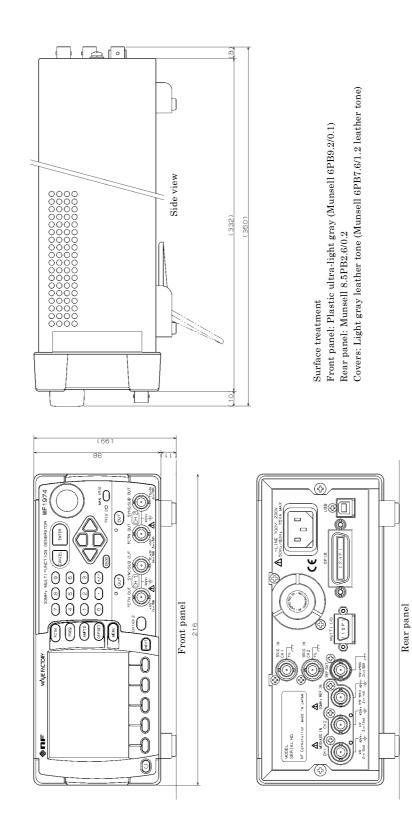


External dimensions (WF1973)



Rear panel

External dimensions (WF1974)



WF1973 / WF1974 Specifications

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